Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	158	("307"/\$ "320"/\$).ccls. and ((charging charger) same battery same load) and @pd>="20070413"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/24 17:56
L3	93	("307"/\$ "320"/\$).ccls. and ((charging charger) with battery with load) and @pd>="20070413"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/24 17:57
L4	53	("307"/\$ "320"/\$).ccls. and ((converter regulator) with battery with load) and @pd>="20070413"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/24 17:57
L5	1	("307"/\$ "320"/\$).ccls. and (charger with convert with battery with load) and @pd>="20070413"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/24 17:58
L6	10	307/65.ccls. and @pd>="20070413"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/24 17:59
L7	34	307/66.ccls. and @pd>="20070413"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/24 18:00
L8	0	nguyen-don.in. and @pd>="20070413"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/24 18:00

			T	7		
L9	5	nguyen-don-j.in. and @pd>="20070413"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/24 18:01
L10	0	rose-bruce.in. and @pd>="20070413"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/24 18:01
L11		rose-bruce-w.in. and @pd>="20070413"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/24 18:01
L12	0	(mains adj voltage) with (battery adj voltage) with ("same" adj range) and @pd>="20070413"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/24 18:02
L13	0	(mains adj voltage) near3 "same" near3 (battery adj voltage) and @pd>="20070413"	US-PGPUÉ; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/24 18:03
L14	0	((ac dc) adj voltage) with (battery adj voltage) with ("same" adj range) and @pd>="20070413"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/24 18:03
L15	0	((ac dc) adj voltage) near3 "same" near3 (battery adj voltage) and @pd>="20070413"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/24 18:03

L16	6	307/150.ccls. and	US-PGPUB;	OR	ON	2007/10/24 18:04
		@pd>="20070413"	USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB			
L17	24	"307"/\$.ccls. and (battery adj charger) and @pd>="20070413"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/24 18:05
L18	1	((second adj battery) same (selec\$5 near2 switch) and ac) and @pd>="20070413"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/24 18:05
L19	1	"320"/\$.ccls. and (switch transistor mosfet fet (mos adj transistor)) and (charger or (voltage adj regulator)) and (second adj battery adj pack) and @pd>="20070413"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/24 18:07
L20	95	(secondary backup uninterruptible uninterruptable ups back-up) same battery same (ac mains utility grid) same (range (upper adj bound) (lower adj bound)) and @pd>="20070413"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/24 18:07
L21	17	(secondary backup uninterruptible uninterruptable ups back-up) same battery same (ac mains utility grid) same (range (upper adj bound) (lower adj bound)) same "same" and @pd>="20070413"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/24 18:08
L22	31	307/64.ccls. and @pd>="20070413"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/24 18:08

L23	0	(second adj dc adj (power source)) same ("same" near2 voltage adj range) and @pd>="20070413"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/24 18:09
L24	0	(second adj dc adj (power source)) same (common adj (voltage limit)) and @pd>="20070413"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/24 18:09

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L25	1	(input and (power rail) and battery and voltage and (regulator charger) and (switch switching) and control and port and (battery adj stack) and signal).clm.	US-PGPUB	OR	ON	2007/10/24 18:11
L26	1	(input and (power rail) and battery and voltage and (regulator charger) and (switch switching) and control and (battery adj stack) and signal).clm.	US-PGPUB	ÓR	ON	2007/10/24 18:11
L27	2	(input and (power rail) and battery and voltage and (regulator charger) and (switch switching) and control and (battery adj stack)).clm.	US-PGPUB	OR	ON	2007/10/24 18:12
L28	1	(input and (power rail) and battery and voltage and (regulator charger) and (switch switching) and control and port and (battery adj stack)).clm.	US-PGPUB	OR	ON	2007/10/24 18:12
L29	0	(input and (power rail) and battery and voltage and (regulator charger) and (switch switching) and control and terminal and (battery adj stack)).clm.	US-PGPUB	OR	ON	2007/10/24 18:12